

RAJENDRA BAHADUR SINGH

Research/Faculty Associate
Department of Electronics & Communication
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Work Experience

- ❖ I have worked as a Lecturer in **BBDNITM**, Lucknow in EC Department (duration 25 Feb 2010 to 28 Feb 2011).
- ❖ I have worked as a Lecturer in Saroj Institute of Technology & Management (SITM), Lucknow in EC Department (duration 14 May 2009 to 24 Feb 2010).
- ❖ I done job in **Coreel Technology**, Bangalore as an Intern (duration 15 Sep 2008 to 20 March 2009)
- ❖ I have done Internship program at **Central Electronics Engineering Research Institute (CEERI)**, Pilani, and Rajasthan (duration Sep 2007 to April 2008) as a Trainee in **IC Design group**.

Education Qualification

Certificate/Degree	University/Board	Year	Division
Ph.D(Pursuing)	Gautam Buddha University, Gr.Noida	Pursing	
M.Tech –E.C.(VLSI Design)	Nirma University, Ahmedabad	2008	First
B.Tech (Electronics & Telecomm.)	Uttar Pradesh Technical University (U.P.T.U.), Lucknow	2005	First
Intermediate	U.P.Board	2001	First
High-School	U.P.Board	1999	First

Research Publications: Journal Papers

- ❖ Rajendra Bahadur Singh, Anurag Singh Baghel, “Simulated Annealing algorithm for VLSI floorplanning for soft blocks”, International Journal of Computer Science and Information Security, April 2018.
- ❖ Kumar Abhinav Rajput, Rajendra Bhadur Singh, “Reduce Leakage Power of SRAM Cell using Variable Body Biasing and Force Sleep Tecnique in Submicron Technology”, International Journal of Computer Networks and Wireless Communications (IJCNWC), ISSN: 2250-3501 Vol.6, No 3, May-June 2016.
- ❖ Akash Kumar, Rajendra Bhadur Singh, “Asymmetric 6T SRAM cells using a boosted bit line write assist technique”, International Journal of Computer Science and Information Technology & Security (IJSITS), ISSN: 2249-9555 Vol.6, No3, May-June 2016.
- ❖ Kumari Pritee, Rajendra Bhadur Singh, “5-bit Folded Interpolated ADC in 90-nm CMOS Technology”, International Journal of Computer Networks and Wireless Communications (IJCNWC), ISSN: 2250-3501 Vol.6, No 3, May-June 2016.

- ❖ Sanjay Kumar Chauhan, Rajesh Mishra, Rajendra Bhadur Singh, “Estimation of Software Reliability on the Basis of Bits for Embedded System”, International Journal of Engineering Research and General Science Volume 4, Issue 2, March-April, 2016 ISSN 2091-2730.
- ❖ Prateek Khurana, Rajat Arora, Monika Nagaria, Megha Sharma, Rajendra Bhadur Singh, “FPGA-Based Interfacing For 8-Bit and 32-Bit Electronic Devices”, IJRET: International Journal of Research in Engineering and Technology eISSN: 2319-1163 , pISSN: 2321-7308, Volume: 03 Issue: 05, May-2014.

Research Publications: Conference Papers

- ❖ Ayush Agarwal, Rajendra Bahadur Singh, “Comparative Analysis of Traveling Salesman Problem using Metaheuristic Algorithms, Proceedings of International Conference on Communication and Computing Systems By Taylor 849-853, 2017.
- ❖ Rajendra Bahadur Singh, Anurag Singh Baghel, Ayush Agarwal, “A review on VLSI floorplanning optimization using metaheuristic algorithms”, 2016 International Conference on Electrical, Electronics, and Optimization Techniques (ICEEOT), Chennai.
- ❖ Neeraj Kumar Niranjana, Rajendra Bahadur Singh, Navaid Z. Rizvi, “Parametric analysis of a hybrid 1-bit full adder in UDSM and CNTFET technology” , International Conference on Electrical, Electronics, and Optimization Techniques (ICEEOT), Chennai 2016.

Short Term Courses (FDP/Workshop) Attended

- ❖ Two week FDP on Swachhta Pakhwada organized by NSS & DSA at Gautam Buddha University, Gr. Noida from 1 Sept to 15 Sept 2017.
- ❖ One week short term training programme on “Application of Techniques/Tools in Engineering”, Gautam Buddha University, Greater Noida, 9th -16th October, 2017.
- ❖ One week faculty development programme on “Recent Trends of Soft Computing Techniques in Software Engineering and Applications” FDP-SCTSEA, Gautam Buddha University, Greater Noida, 19th -24th December, 2016.
- ❖ Two Weeks Workshop on “Signals and Systems”, conducted by IIT Kharagpur, 2th-12th Jan 2014 at Jaypee Institute of Information Technology, Noida.
- ❖ International Workshop for Cloud Computing & its Applications (IWCCA), GBU, G.Noida, 17th Feb. 2013.
- ❖ Three days workshop on, “High Vacuum, Plasma & Thin film Deposition for IC Technology” in Technical Association with Technology Application Service, GBU. G.Noida, 7th -9th February, 2013.
- ❖ Two Weeks Workshop on QIP, Creativity & Innovation, IIT Delhi, 4th-18th Dec 2012.
- ❖ One day Workshop on Latex, SOICT G.B.U., Gr.Noida 2016.

Subject Taught

- ❖ Digital Electronics
- ❖ Basic Electronics
- ❖ Linear IC Applications
- ❖ Digital Logic Design
- ❖ CMOS VLSI Design
- ❖ Analog & Communication
- ❖ Digital IC Design
- ❖ Antenna and Wave Propagation
- ❖ Micro-electronics

Academic Thesis Supervised

Ph.D: NIL

M.Tech: 45 Completed, 6 ongoing.

Expert Lecture and Industrial Visit conducted

- ❖ Doordarshan Visit of B.Tech(EC) second year students on 23 April 2014, Delhi.
- ❖ Expert Lecture on “Network Lifetime Maximization in Directional Sensor Networks” , by Dr. Alok Singh, Associate Professor, School of Computer and Information Science, University of Hyderabad, 1st April, 2015.

Academic Administration

- ❖ Observer for Gautam Buddha University entrance examination in May 2018.
- ❖ **Faculty In-charge of the** Digital Electronics Lab and Linear IC Lab, in School of ICT, G.B.U. Greater Noida.
- ❖ **Member**, University Examination Team, **Gautam Buddha University** (2013-2015).
- ❖ **Warden**, Sant Kabir Das Hostel, **Gautam Buddha University**, From 26 Oct 2017 to till.
- ❖ **Associate Warden**, Sant Kabir Das Hostel, **Gautam Buddha University** (from Sept 2016 to 25 Oct 2017)
- ❖ **Assistant Warden**, Narayan Guru Hostel, Gautam Buddha University, (from Aug 2014 to July 2015).
- ❖ **Member**, School of ICT Examination Team, **Gautam Buddha University** (2012-13 & 2015-17).
- ❖ **Paper Setter and Valuer** for **Gautam Buddha University** Gr. NOIDA.

Skills

- Language : C, Matlab
- Hardware Description Languages(HDLs): VHDL
- EDA Tools: Xilinx-ISE, ChipScope, Mentor Graphics-Modelsim, Microwind, Tanner,

Reference

Dr. Anurag Singh Baghel
Assistant Professor
Dept. of Computer Science,
School of ICT
G.B.U,Gr.Noida

Dr. Rajesh Mishra
Assistant Professor
Dept. of Electronics & Comm.
School of ICT,
G.B.U, Gr.Noida

I hereby declare that the above information is true and best of my knowledge.

Date: 20/08/2018

(Rajendra Bahadur Singh)

Place: Greater Noida